Surface mount transient voltage suppressor power 400 watts

Stand-Off Voltage: 200 V~220 V

FEATURES

- For surface mounted applications in order to optimize board space.
- · Low profile package
- · Glass passivated junction
- · Excellent clamping capability
- · Low incremental surge resistance

MECHANICAL DATA

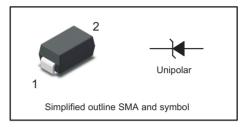
· Case: SMA

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.055g / 0.002oz

PINNING

PIN	DESCRIPTION					
1	Cathode					
2	Anode					



Maximum Ratings and Electrical characteristics Ratings at 25 °C ambient temperature unless otherwise specified.

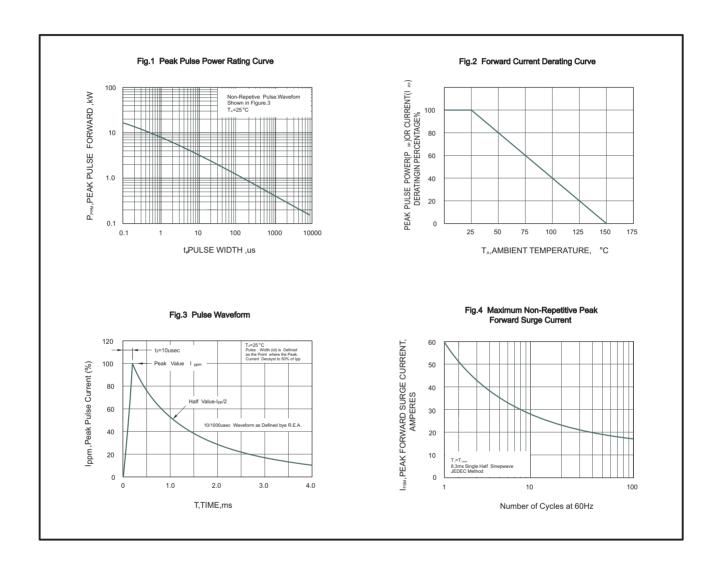
Parameter	Symbol	Value	Unit
Peak Pulse Power Dissipation on 10/1000 s waveform (Note1,Note2, Fig.1).	P _{PPM}	400	W
Peak Forward Surge Current,8.3ms Single Half Sine-Wave Superimposed on Rated Load, (JEDEC Method) (Note 3,Fig4).	I _{FSM}	60	А
Peak Pulse Current on 10/1000 us waveform (Note 1, Fig 3)	I _{PPM}	see Table 1	Α
ESD Voltage per IEC61000-4-2 Contact Air	V _{ESD1} ±30 V _{ESD2} ±30		kV
Typical Thermal Resistance Junction to Ambient(Note 2)	$R_{\theta JA}$	100	°C/W
Operating Junction Temperature and Storage Temperature Range	T_{j},T_{stg}	-55 ~ +150	°C

NOTES:

- 1. Non-repetitive current pulse, per Fig.3 and derated above $T_A = 25\,^{\circ}\text{C}\,$ per Fig. 2.
- 2. Mounted on 5x5 mm (0.13mm thick) land areas.
- 3. Measured on 8.3ms, single half sine-wave or equivalent square wave, duty cycle=4 pulses per minute maximum.

Characteristics at Ta = 25°C Table 1

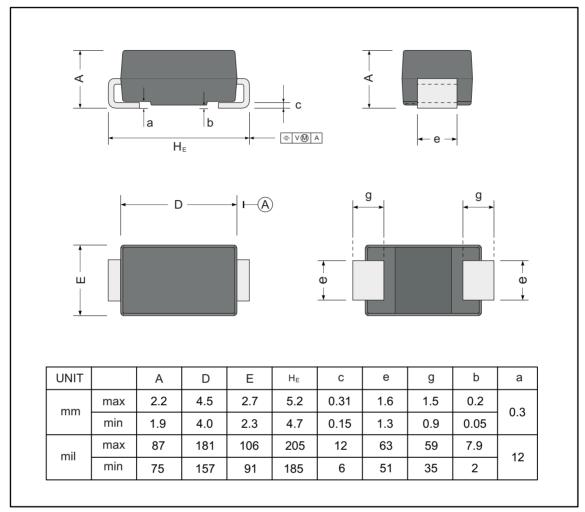
Туре	Marking	V _{RMW}		Breakdown Test Voltage Current		Reverse Leakage	Max. Clamp Voltage	Peak Pulse Current
			V _{BR} @ I _T		1	I _R @ V _{RWM}	V _C @ I _{PP}	l
			Min	Max	т'	IR C VRWM	VC Œ IPP	Грр
UNI	UNI	V	V	V	mA	μA	V	A
SMAJ200AA	sv	200	224	247	1	1	324	1.2
SMAJ220AA	SX	220	246	272	1	1	356	1.1



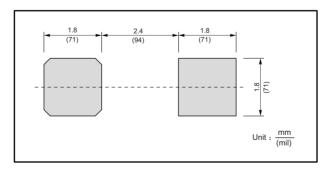
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SMA



The recommended mounting pad size



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